



MoE's
INNOVATION CELL
(GOVERNMENT OF INDIA)



SMART INDIA
HACKATHON
2025



INSTITUTION'S
INNOVATION
COUNCIL
(Ministry of Education Initiative)

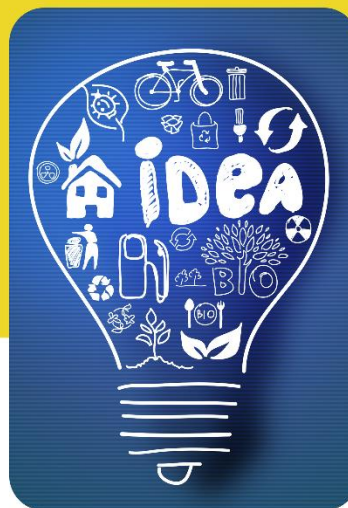


Smart India Hackathon (SIH)2025

Ready to innovate for India?



Join our Internal Hackathon and get a
chance to represent the college at SIH
2025 – India's biggest innovation
challenge!



WHY PARTICIPATE?

- ✓ National-level recognition
- ✓ Work on real-world problems
- ✓ Mentorship & networking
- ✓ Travel & accommodation for finalists

WHO CAN APPLY?

- Teams of 6 students from the same college
- At least 1 female member in each team
- Open to all branches & years

APPLY NOW



Last Date:
06th September 2025

ORGANIZED BY

Cambrian Incubation Center (CIC)

Tech-Driven. Innovation-Fuelled. Cambrian-Powered.



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Convenor &SPOC

Dr. Varalatchoumy M
Head-CIC & CHOSS
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Internal Hackathon Report – SIH 2025

Venue: CIC, 5th Floor, Sir M.V Block, CIT

Date of Event: 18th & 19th September 2025

Time: 10:00 AM – 5:00 PM & 10:00 AM- 12:30 PM

1. About the Event

The **Internal Hackathon for SIH 2025** was organized to identify and nurture innovative student ideas within the Cambridge Institute of Technology. This event served as the gateway for our students to represent the institution at the **Smart India Hackathon (SIH) 2025**, a national-level innovation challenge initiated by the Ministry of Education, Government of India.

The hackathon encouraged students to work in teams, brainstorm solutions for real-world problem statements, and showcase their creativity, technical skills, and teamwork.

2. About the Problem Statements

Problem statements were designed in alignment with the **SIH 2025 themes** and reflected **real-world industry and societal challenges**. Each team had the opportunity to select a problem statement and propose innovative, feasible, and impactful solutions.

3. Participation Statistics

- **Total Teams Registered:** 77
- **Total Students Participated:** 69

Gender-wise Participation:

- Male: 200
- Female: 214

Team Composition: Each team had **6 members with at least one female participant**, as mandated by SIH guidelines.

4. Event Flow

- **Inauguration Ceremony** – Opening remarks by institute leadership.
- **Briefing Session** – Orientation on SIH rules, problem statements, and evaluation criteria.
- **Hackathon Rounds** – Teams worked on their solutions and interacted with mentors.
- **Presentations** – Each team shared their solution to the jury panel.
- **Evaluation & Results** – Jury assessed teams and identified the top performers.
- **Valedictory Session** – Announcement of shortlisted teams and closing remarks.

5. Jury Panel & Judges

The event was graced by a distinguished panel of jury members, consisting of experts from academia and industry.

- **Shri. Srinivas Jamkhandi** – Project Scientist, MSME-CoE, Dept. of ESE (formerly CEDT), IISc Bengaluru
- **Shri. B. S. Srinivasan** – Chairman, Industry-Academia Connect, Laghu Udyog Bharati & MD, Viprov Electronics Pvt. Ltd., Bengaluru
- **Dr. Varalatchoumy M** – Professor & Head – AIML, and Head – CIC, Cambridge Institute of Technology
- **Mr. Abishek Samuel** – CEO, Zunofy Capital LLC

Jury Profile

- **Shri. Srinivas Jamkhandi** – Project Scientist, MSME-CoE, Dept. of ESE (formerly CEDT), IISc Bengaluru

He is currently working as a Project Scientist, in the Dept of Electronic Systems Engineering, at Indian Institute of Science, Bangalore, coordinating all the activities of the MSME Centre of Excellence at IISc, Bangalore. His current assignments include identifying challenging design projects from the industry, especially MSME sector and supporting them till successful prototype. He also coordinates innovation-led design projects for M.Tech Students at the Institute. At present, he is...

1. Fellow of the Institute of Engineers (India).
2. Member, Board of Governors, National Design & Research Forum (NDRF).
3. Member, Advisory Board, Dassault Centre of Excellence, Vellore.

Shri S. M. Jamkhandi is a 1982-batch electrical engineering graduate from BMS College of Engineering, Bangalore and later on continued his graduate research in high power, short-circuit arc-interruption techniques in vacuum media at USA. He was earlier the graduate member of the prestigious Society of Manufacturing Engineers, Ohio, USA. He was a recipient of 'Best Graduate Research Student' award from the then Governor of the Ohio State for his research in Short circuit arc interruption in vacuum-media.

His vast experience includes working in industry, teaching and research in academic institutions and policy planning and implementation, including promotional scheme development at the Ministry of MSME, Govt of India. His long career includes successful design and development of a medium voltage Vacuum Circuit Breaker, for which he received an award from the then Karnataka Government. This product was, at that time, the world's lowest energy content breaker developed at 88-Joules. This Vacuum Circuit Breaker is now currently being extensively used throughout Karnataka.

Later on, he joined Crompton Greaves Ltd as Corporate Technical Marketing Manager before joining Civil Services at Government of India as Director in the Ministry of Industry (MSME) in the rank of Deputy Secretary to Government of India. During this tenure in the government, he was involved in the development and implementation of many promotional schemes to support technology and innovation. He has also worked as Director (MSME) in charge of Maharashtra, Karnataka, Tamilnadu and Pondicherry, before he took retirement in 2017.

- **Shri. B. S. Srinivasan** – Chairman, Industry-Academia Connect, Laghu Udyog Bharati & MD, Vipprof Electronics Pvt. Ltd., Bengaluru

He is Managing Partner, Vipprof Electronics with over four decades of pioneering leadership at Vipprof Electronics, I have been at the forefront of India's indigenous technology evolution, specializing in Telecommunications, Defence Electronics, and Embedded Systems. My 41+ years of hands-on innovation and strategic enterprise building have positioned Vipprof as a trusted partner for mission-critical projects involving secure communication systems, lithium-ion battery solutions, and advanced IoT applications.

Key Achievements & Contributions:

- Designed and manufactured telecommunication equipment for Indian Defence and secure networks.
- Developed custom lithium-ion battery packs for sensitive defence applications.
- Delivered solar-powered lighting systems for sustainable infrastructure.
- Engineered embedded systems tailored for industrial and defence applications.
- Created IoT-enabled smart devices for real-time monitoring and automation.

Rooted in technological self-reliance, integrity, and quality, my work has long reflected the principles of "Make in India," driving innovation and supporting national priorities.

Leadership & Personality:

I am recognized for being technically astute, strategically grounded, and committed to nation-building through innovation.

My leadership style emphasizes:

- Hands-on problem solving in complex electronic system design.
- Mentorship of young engineers, fostering the next generation of technology leaders.
- Building long-term, trust-based relationships with clients, vendors, and collaborators.
- Upholding ethics, precision, and continuous improvement as core principles.

My ability to stay agile and forward-looking has enabled Viprof Electronics to evolve alongside emerging technologies—from analog telecom to digital defence systems and smart IoT platforms.

- **Dr. Varalatchoumy M** – Professor & Head – AIML, and Head – CIC, Cambridge Institute of Technology

She is a seasoned academician and research leader with extensive experience in **Artificial Intelligence, Machine Learning, Generative AI, and Computer Vision**. She has authored **4 textbooks, 30+ journal and conference publications**, and holds **15 patents**. Her work has been recognized with multiple awards, including **Research Excellence Award (2019), Best Book Author Award**, and **Distinguished Educator Award (2023)**.

She leads **Cambrian Incubation Centre & CHOSS**, nurturing student startups through mentorship, strategic planning, and industry collaboration. She has organized innovation events like **SEEDBRAINS 1.0, 2.0, 3.0, & 4.0** and facilitated **MSME Host Institute recognition** for student entrepreneurship at CIT.

She serves as a **reviewer for IEEE and international journals**, convenes conferences, guides research scholars, and leads industry-focused AI/ML projects. She is an **AICTE-certified Innovation Ambassador** and has played a key role in creating internships, industry connect, and real-world project opportunities for students.

Her strengths include **leadership, critical thinking, innovation, and multi-disciplinary problem-solving**, combined with a commitment to fostering the next generation of technologists and entrepreneurs.

- **Mr. Abishek Samuel** – CEO, Zunofy Capital LLC

He is a tech entrepreneur turned venture strategist, blending deep technical expertise with a passion for building meaningful ventures. Starting in tech services, he delivered **200+ projects** across MNCs, SMEs, and governments, before founding **NXCRW**, a venture studio transforming early-stage ideas into robust, scalable tech.

He also founded **Hoverbean**, evolving from a creator community into a venture-backed blockchain protocol, and **Zunofy**, which tackles niche challenges in global banking, offering zero FX markup, zero fees, and virtual IBAN solutions.

A part-time poet and weekend musician, Abishek values **creative thinking, deep conversations, and connecting with entrepreneurs building unconventional, high-impact tech**. He was recently honoured with the **Bharat Gaurav Ratna Shri Samman Award** for his contributions to innovation and entrepreneurship.

6. Judging Process

Each team was evaluated independently by the jury members using a **standardized evaluation sheet**. The evaluation was based on the following criteria (5 marks each, max 25):

1. **Problem Understanding**
2. **Innovation & Creativity**
3. **Technical Feasibility**
4. **Impact & Usefulness**
5. **Presentation**

Scores were consolidated to generate a **merit ranking** of all participating teams

7. Evaluation Criteria

Criteria	Description	Max Marks
Problem Understanding (PS)	Clarity of the problem definition and its relevance	5
Innovation & Creativity(IC)	Novelty, originality, uniqueness of the idea	5
Technical Feasibility(TC)	Practicality, implementation possibility, tech soundness	5
Impact & Usefulness(IU)	Social/industry relevance, scalability, value addition	5
Presentation(PP)	Clarity of explanation, structure, communication, teamwork	5
Total		25

8. Nominated Top Teams

Based on the evaluation process, the following nominations were made for **SIH 2025**:

- **45 Teams Shortlisted**
- **5 Teams Waitlisted**

Gender-wise Participation:

- Male: 165
- Female: 135

SIH Shortlisted Team

SL No	ID	Name	PS ID	SL No	ID	Name	PS ID
1	SIHCIT49	saadaxa	SIH25047	26	SIHCIT20	Runtime Terror 2	SIH25052
2	SIHCIT61	CuraLink	SIH25026	27	SIHCIT26	Innov8ors	SIH25019
3	SIHCIT02	Smart Mycopurifier	SIH25036	28	SIHCIT27	Crop Compass	SIH25030
4	SIHCIT40	DigiDost	SIH25048	29	SIHCIT30	SwiftRails	SIH25026
5	SIHCIT67	Innomavericks	SIH25046	30	SIHCIT47	Sukh Sehat	SIH25018
6	SIHCIT38	The Precision Crew	SIH25014	31	SIHCIT59	PrakritiChain	SIH25027
7	SIHCIT63	AppCrafters	SIH25050	32	SIHCIT66	VISIONEERS	SIH25002
8	SIHCIT64	WeTrack	SIH25011	33	SIHCIT33	Travel Safe Nexus	SIH25002
9	SIHCIT68	AyushSync	SIH25026	34	SIHCIT51	Strivers	SIH25034
10	SIHCIT06	Team Zero	SIH25039	35	SIHCIT43	Bitbots	SIH25030
11	SIHCIT15	AgrivigilX	SIH25015	36	SIHCIT44	Innovengers	SIH25016
12	SIHCIT62	Latte & Logic	SIH25041	37	SIHCIT54	Deep Drivers	SIH25043
13	SIHCIT69	NextGen Traffic	SIH25050	38	SIHCIT65	Tech Titans	SIH25048
14	SIHCIT39	Google it	SIH25010	39	SIHCIT23	Sarathi Squad	SIH25013
15	SIHCIT08	Hydro Assess	SIH25065	40	SIHCIT42	Telemedicine	SIH25018
16	SIHCIT16	S5RV	SIH25060	41	SIHCIT45	Disaster	SIH25028
17	SIHCIT31	Binary Brains	SIH25044	42	SIHCIT46	Hexapods	SIH25082
18	SIHCIT35	ChronoLens	SIH25052	43	SIHCIT48	LeafSense	SIH25015
19	SIHCIT52	RideX	SIH25013	44	SIHCIT57	Robo Rebels	SIH25117
20	SIHCIT60	GEN 64	SIH25021	45	SIHCIT29	Glacier	SIH25021
21	SIHCIT04	NextGen Coders	SIH25021	46	SIHCIT41	code blooded	SIH25044
22	SIHCIT07	Hack Attack	SIH25047	47	SIHCIT55	Electro Nova	SIH25056
23	SIHCIT11	Runtime Terror	SIH25049	48	SIHCIT05	Urbansentinels	SIH25050
24	SIHCIT14	BlueVerse	SIH25040	49	SIHCIT09	SaHaayak	SIH25031
25	SIHCIT18	Vigil	SIH25002	50	SIHCIT19	jackfruit jugaad	SIH25033

These teams will be uploaded on the official SIH portal for further consideration.

9. Event Photos



In Association